



Attorney's Document No.: 890.P2003DC

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Alexander Peleg, et al.

Application No. 09/657,448

Filed: September 8, 2000

For: MICROPROCESSOR HAVING
NOVEL OPERATIONS

Examiner: K. Kim

Art Unit: 2183

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Judy L. Steinkraus

Date

Commissioner For Patents
Washington, D.C. 20231-0001

AMENDMENT AND RESPONSE TO RESTRICTION REQUIREMENT

Sir:

Applicants respectfully requests that the above-identified application be amended as follows:

IN THE CLAIMS:

Please add the following new claims:

24. (New) The apparatus of Claim 15, the unpack instruction having an Intel integer opcode format comprising three bytes, a third byte of the three bytes permitting a first source register address and a second source-destination register address.

10/15/2001 EPOVERK1 00000049 09537448

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